

Essential Patents Listed on October 1, 2008 Attachment 1 for IEEE 1394 and Related Standards

This chart shows *illustrative* essential claims of each patent. (Other patent claims also may be essential).

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
US 5,052,029	Claim 1 is essential for transmitting a cycle start packet with a value indicating a delay in starting the cycle.	IEEE 1394-1995 (Aug. 30, 1996) <i>Secs.:</i> 3.6.4, 6.2.2.2.3, 6.3.1.5, 6.3.2, 6.3.3, 6.3.3.3, 8.3.2.3.1 <i>Figs.:</i> 3-16, 6-10, 6-19, 8-5 <i>Tables:</i> 6-14 <i>Pages:</i> 32, 33, 148, 149, 164-166, 169-170, 211, 212
US 5,362,249	Claim 1 is essential for an electrical connector with an ESD feature.	IEEE 1394-1995 (Aug. 30, 1996) <i>Secs.:</i> 4.2.1.1.1 <i>Figs.:</i> 4-2 to 4-5 <i>Pages:</i> 55-59
US 5,384,769	Claim 1 is essential for interpreting arbitration signals and deriving an arbitration state from 0, 1 or Z values driven by, and detected at, a port.	IEEE 1394-1995 (Aug. 30, 1996) <i>Secs.:</i> 4.2.2, 4.3.2, 4.3.3 <i>Fig.:</i> 4-12 <i>Tables:</i> 4-25 to 4-28 <i>Pages:</i> 76, 84, 85
EP 689,743 (DE, FR, GB)	Claim 8 is essential for interpreting arbitration signals and deriving an arbitration state from 0, 1 or Z values driven by, and detected at, a port.	IEEE 1394-1995 (Aug. 30, 1996) <i>Secs.:</i> 1.1, 3.1, 3.4.1, 3.6, 3.7.3.1.1, 3.7.3.4, 4.2.2, 4.2.2.4, 4.3.2, 4.3.3 <i>Fig.:</i> 4-12 <i>Tables:</i> 4-25 to 4-28 <i>Pages:</i> 1, 19, 23, 76, 84, 85
JP 3,454,511	Claim 1 is essential for interpreting arbitration signals and deriving an arbitration state from 0, 1 or Z values driven by, and detected at, a port.	IEEE 1394-1995 (Aug. 30, 1996) <i>Secs.:</i> 4.2.2, 4.3.2, 4.3.3 <i>Fig.:</i> 4-12 <i>Tables:</i> 4-25 to 4-28 <i>Pages:</i> 76, 84, 85

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
US 5,384,808	Claim 7 is essential for connecting a PHY circuit to a link layer circuit across an isolation barrier.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: J.2, J.6 Figs.: J-10, J-11 Pages: 333, 334, 347, 348</p> <p>IEEE 1394a-2000 (Jun. 30, 2000) Secs.: 5A Fig.: 5A-1 Pages: 98-100</p>
EP 677,191 (DE, FR, GB, IT, SE)	Claim 1 is essential for connecting a PHY circuit to a link layer circuit across an isolation barrier.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 2.2.52, 3.7.1, 5, J.2, J.6 Figs.: J-10, J-11 Pages: 16, 34, 113, 333, 334, 347, 348</p> <p>IEEE 1394a-2000 (Jun. 30, 2000) Secs.: 5A Figs.: 5A-1, 5A-2, 5A-19 Table: 5A-1 Pages: 98-100, 122</p>
JP 3,399,950	Claim 1 is essential for connecting a PHY circuit to a link layer circuit across an isolation barrier.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: J.2, J.6 Figs.: J-10, J-11 Pages: 333, 334, 347, 348</p>

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
US 5,394,556	Claim 1 is essential for initiating the self_ID process, and for choosing a PHY_ID for each node.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 1.1, 3.7.3, 3.7.3.1.2, 3.7.3.1.3, 4.2, 4.2.3.1, 4.4.2.2, 4.4.2.2.1, 4.4.2.2.2, 4.4.2.3, 4.4.2.3.1, 4.4.2.3.2, 4.4.2.4.1 Figs.: 3-2, 3-21, 3-23, 3-24, 4-1, 4-23, 4-24 Tables: 4-45, 4-46 Pages: 1, 20, 36-40, 53-54, 100-106, 109
EP 674,789 (DE, FR, GB)	Claim 1 is essential for initiating the self_ID process, and for choosing a PHY_ID for each node.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 1.1, 3.7.3, 3.7.3.1.1, 3.7.3.1.2, 3.7.3.1.3, 3.7.3.2, 4.2, 4.4.2.2, 4.4.2.2.1, 4.4.2.3.1 Figs.: 3-2, 3-22, 3-23 Pages: 1, 20, 36-43, 53-54, 100-106, 109
JP 3,243,613	Claim 1 is essential for initiating the self_ID process, and for choosing a PHY_ID for each node.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.7.3, 3.7.3.1, 3.7.3.1.1, 3.7.3.1.2, 3.7.3.1.3, 4.4.2.1, 4.4.2.2, 4.4.2.3 Figs.: 4-23, 4-24 Pages: 36-40, 98, 100, 103
KR 316,208	Claim 1 is essential for initiating the self_ID process and for choosing a PHY_ID for each node.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.1, 3.2.1, 3.7.3, 3.7.3.1.2, 3.7.3.1.3, 4.4.2.2, 4.4.2.2.1, 4.4.2.2.2, 4.4.2.3, 4.4.2.3.1, 4.4.2.3.2 Figs.: 3-1, 3-2, 3-21 to 3-24, 4-23, 4-24 Pages: 19-20, 36-40, 100-106
US 5,400,340	Claim 1 is essential for a port with a resynchronizer that resynchronizes data to a local clock of the port.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.7.3, 4.4, 4.4.1.2 Figs.: 3-21, 4-21 Tables: 4-37, 4-41 Pages: 36-37, 92, 93, 96
US 5,485,488	Claim 13 is essential for speed signaling during the data prefix of a PHY packet.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 4.2.1.2.1, 4.2.2, 4.2.2.1-4.2.2.3, 4.3.1, 4.3.2, 4.3.3, 4.4.1 Figs.: 4-8, 4-12, 4-14, 4-17 Pages: 65, 76-79, 83-85, 89, 94

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
US 5,493,570	Claim 1 is essential for a port to detect the end of a received packet.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 4.2, 4.4, 4.4.1.2, 4.4.2.4.1 Figs.: 4-1, 4-21, 4-25 Tables: 4-41, 4-47 Pages: 53-54, 92, 96, 107, 108, 111
US 5,495,481	Claim 7 is essential for performing ack-accelerated arbitration.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.1, 3.6, 4.4.2.4, 4.4.2.4.1, 4.4.2.4.2, 6.2.1, 6.2.5 Figs.: 3-1, 3-8, 4-25 Tables: 4-47 Pages: 19, 26-27, 107-109, 111, 144, 161 IEEE 1394a-2000 (Jun. 30, 2000) Secs.: 3.9.2.2, 4.4.3 Pages: 17, 72
US 5,559,967	Claim 8 is essential for transmitting a disabling signal, as opposed to a packet, to a node incapable of receiving the packet at the packet's requisite speed.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.1, 3.2.1, 3.7.3.3, 4.2.2, 4.2.2.3, 4.4, 4.4.1.1 Figs.: 3-1, 4-12, 4-14, 4-21 Tables: 4-17, 4-38, 4-39 Pages: 19, 20, 43, 76, 78, 79, 93, 94
US 5,579,486	Claim 1 is essential for a PHY which selectively couples either arbitration signals or data signals to and from the port.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.1, 3.2.1, 3.7.1, 4.2, 4.3.2, 4.4 Figs.: 4-1, 4-21 Pages: 19-20, 34, 53-54, 84, 92-93
EP 679,275 (DE, FR, GB)	Claim 1 is essential for a PHY that couples arbitration signals or data signals to and from the port.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 2.2.5, 3.2.1, 4.2, 4.4 Figs.: 4-1, 4-21 Pages: 13, 20, 53, 54, 92
JP 3,571,340	Claim 1 is essential for a PHY which selectively couples either arbitration signals or data signals to and from the port.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.1, 3.2.1, 3.7.1, 4.2, 4.3.2, 4.4 Figs.: 4-1, 4-21 Pages: 19-20, 34, 53-54, 84, 92-93

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
US 5,630,173	Claim 1 is essential for fair arbitration.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 1.1, 3.1, 3.2.1, 3.7.2, 3.7.3, 3.7.3.1.2, 3.7.3.2, 4.2, 4.4.2.4, 4.4.2.4.1, 4.4.2.4.2 Figs.: 3-1, 3-2, 3-19, 3-20, 3-21, 3-23, 3-25 to 3-29, 4-1, 4-25 Tables: 4-47 Pages: 1, 19-20, 35-43, 53-54, 106-112
JP 3,638,949	Claim 1 is essential for fair arbitration.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.7.2, 3.7.3.2, 4.4.2.4 Figs.: 3-19, 3-20, 3-25 to 3-29, 4-25 Pages: 35, 36, 40 to 43, 106, 107
KR 290,517	Claim 1 is essential for fair arbitration.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.1, 3.2.1, 3.7.3.1.2, 3.7.3.2, 4.2.4, 4.4.2.4.1, 4.4.2.4.2 Figs.: 3-1, 3-2, 3-21, 3-23, 3-25 to 3-29 Pages: 19-20, 38-43, 106-112
US 5,802,048	Claim 1 is essential for performing ack-accelerated arbitration.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.1, 3.6, 4.4.2.4, 4.4.2.4.1, 4.4.2.4.2, 6.2.5, 6.3.3.2 Figs.: 3-1, 3-8, 4-25, 6-18 Tables: 4-47 Pages: 19, 26-27, 107-109, 111, 161, 168 IEEE 1394a-2000 (Jun. 30, 2000) Secs.: 3.9.2.2, 4.4.3 Pages: 17, 72

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
US 5,802,057	Claim 6 is essential for fly-by accelerated arbitration.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.1, 3.2, 3.6, 3.7.3.1.1 Figs.: 3-1, 3-2, 3-8, 3-9, 3-22 Pages: 19-20, 26-27, 38</p> <p>IEEE 1394a-2000 (Jun. 30, 2000) Secs.: 3.9.2.3, 4.4.2.2, 4.4.3, 4.4.3.4.1, 4.4.3.4.2, 4.4.3.4.3 Figs.: 3-32, 4-25 Tables: 4-43, 4-47A, 4-47B Pages: 18, 70, 72, 83-85, 87-88</p>
US 5,802,289	Claim 1 is essential for performing the bus reset function at a node.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 1.1, 3.1, 3.2.1, 3.7.3, 3.7.3.1.2, 4.2, 4.4.2.1, 4.4.2.1.1, 4.4.2.1.2 Figs.: 3-1, 3-2, 3-21, 3-23, 4-1, 4-22 Tables: 4-44 Pages: 1, 19-20, 36-39, 53-54, 98-99</p>
US 5,809,331	Claim 1 is essential for implementing the Configuration ROM.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 1.7.1, 2.2.23, 3.2, 3.3, 8.3.2.5, 8.3.2.5.5, 8.3.2.5.5.1 to 8.3.2.5.5.3, 8.3.2.5.6, 8.3.2.5.6.1 Figs.: 3-2, 3-3, 8-21 to 8-24 Pages: 10-11, 14, 20, 21, 221-222, 224-227</p>
US 5,842,027	Claim 24 is essential for power management in a node.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.1, 3.7.3, 4.2.2.7, 4.3.4, 4.3.4.1, 4.4, 8.4.4, 8.4.4.1, 8.4.4.4 Figs.: 3-1, 3-21, 4-16, 4-18, 4-21 Tables: 4-20, 4-29 Pages: 19, 36-37, 81-82, 86-87, 92, 234-235</p>

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
US 6,385,679	Claim 1 is essential for fly-by accelerated arbitration.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.1, 3.2, 3.6, 3.6.2.2, 3.7.3.1.1 Figs.: 3-1, 3-2, 3-8, 3-9, 3-12, 3-22 Pages: 19-20, 26-27, 29, 38</p> <p>IEEE 1394a-2000 (Jun. 30, 2000) Secs.: 3.9.2.3, 4.4.2.2, 4.4.3, 4.4.3.4.1, 4.4.3.4.2, 4.4.3.4.3 Figs.: 3-32, 4-25 Tables: 4-43, 4-47A, 4-47B Pages: 18, 70, 72, 83-85, 87-88</p>
US 6,587,904	Claim 1 is essential for loop prevention.	<p>IEEE 1394b-2002 (Dec. 14, 2002) Sec.: 3.10.5.2, 14.7, 14.7.1, 14.7.6, 14.7.7, 14.7.8, 14.7.9, 16.3.3.5 Fig.: 14-3 Table: 16-8 Pages: 27, 155, 156, 158, 159, 193, 194</p>
US 6,618,785	Claim 1 is essential for performing an autocrossover.	<p>IEEE 1394b-2002 (Dec. 14, 2002) Sec.: 12.4.5, 16.2.5.1 Figs.: 19-3, 19-5, 19-7 Pages: 110, 181, 263, 269, 281, 287, 288</p>
US 6,636,914	Claim 1 is essential for Beta-mode arbitration.	<p>IEEE 1394b-2002 (Dec. 14, 2002) Secs.: 1.6.7, 2.2.x, 3.10.5.2, 3.10.5.2.2, 3.10.5.2.3, 16.4, 16.4.1, 16.4.2, 16.4.2.1, 19, 19.4.1, 19.5 Figs.: 3-34, 19-17, 19-22 Table: 16-11 Pages: 7, 8, 14, 15, 16, 27 to 30, 196, 198, 199, 259, 314, 331, 332, 349, 351, 352</p>

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
US 6,639,918	Claim 1 is essential for Beta-mode arbitration.	IEEE 1394b-2002 (Dec. 14, 2002) Secs.: 0.1, 2.2.x, 3.10.5.2, 3.10.5.2.1, 3.10.5.2.3, 3.10.5.2.4, 16.4.2.1, 16.4.2.2 Figs.: 19-4, 19-22 Table: 16-11, 16-12 Pages: 1, 19, 27 to 30, 198 to 200, 266, 350
US 6,711,173	Claim 1 is essential for fly-by accelerated arbitration.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.1, 3.2, 3.6, 3.7.3.1.1 Figs.: 3-1, 3-2, 3-8, 3-9, 3-22 Pages: 19-20, 26-27, 38 IEEE 1394a-2000 (Jun. 30, 2000) Secs.: 3.9.2.3, 4.4.2.2, 4.4.3, 4.4.3.4.1, 4.4.3.4.2, 4.4.3.4.3 Figs.: 3-32, 4-25 Tables: 4-43, 4-47A, 4-47B Pages: 18, 70, 72, 83-85, 87-88
US 6,718,497	Claim 4 is essential for jitter measurement.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.1, 3.2, 3.6 Figs.: 3-1, 3-2, 3-8, 3-9 Pages: 19-20, 26-27 IEEE 1394b-2002 (Dec. 14, 2002) Secs.: 0.1, 13.3.2.7, 15.1.1, Annex O Fig.: 15-2 Tables: 15-3 Pages: 1, 140, 168 to 171, 365, 366

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
US 6,721,330	Claim 4 is essential for fly-by accelerated arbitration.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.1, 3.2, 3.6, 3.7.3.1.1 Figs.: 3-1, 3-2, 3-8, 3-9, 3-22 Pages: 19-20, 26-27, 38</p> <p>IEEE 1394a-2000 (Jun. 30, 2000) Secs.: 2.2.66, 3.9.2.3, 4.4.2.2, 4.4.3, 4.4.3.4.1, 4.4.3.4.2, 4.4.3.4.3 Figs.: 3-32, 4-25 Tables: 4-43, 4-47A, 4-47B Pages: 13, 18, 70, 72, 83-85, 87-88</p>
US 6,763,414	Claim 1 is essential for fly-by accelerated arbitration.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.1, 3.2, 3.6 Figs.: 3-1, 3-2, 3-8, 3-9 Pages: 19-20, 26-27</p> <p>IEEE 1394a-2000 (Jun. 30, 2000) Secs.: 3.9.2.3, 4.4.2.2, 4.4.3, 4.4.3.4.1, 4.4.3.4.2, 4.4.3.4.3 Figs.: 3-32, 4-25 Tables: 4-43, 4-47A, 4-47B Pages: 18, 70, 72, 83-85, 87-88</p>
US 6,831,928	Claim 1 is essential for transmitting only Legacy format packets into a Legacy cloud.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.1, 3.2, 3.6 Figs.: 3-1, 3-2, 3-8, 3-9 Pages: 19-20, 26-27</p> <p>IEEE 1394b-2002 (Dec. 14, 2002) Secs.: 2.2.x, 3.10.2, 3.10.5.2.4, 16.4.3, 16.4.3.2.1.1 Pages: 15, 17, 23, 30, 200, 201</p>

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
US 6,842,805	Claim 1 is essential for loop prevention.	IEEE 1394b-2002 (Dec. 14, 2002) Sec.: 3.10.5.2, 14.7, 14.7.1, 14.7.6, 14.7.7, 14.7.8, 14.7.9, 16.3.3.5 Figs.: 14-3, 19-6 Table: 16-8 Pages: 27, 155, 156, 158, 159, 193, 194, 275, 276, 278, 279, 280
US 6,865,632	Claim 1 is essential for Beta-mode arbitration.	IEEE 1394b-2002 (Dec. 14, 2002) Secs.: 1.6.7, 2.2.x, 3.10.5.2, 3.10.5.2.2, 3.10.5.2.3, 16.4, 16.4.1, 16.4.2, 16.4.2.1, 19, 19.3.2, 19.4.1, 19.5 Figs.: 3-34, 19-2, 19-5, 19-11, 19-17, 19-18, 19-22 Table: 16-11 Pages: 7, 8, 14, 15, 27 to 30, 196, 198, 199, 259, 261, 270, 301, 314, 327-332, 339, 349-353
US 6,904,044	Claim 1 is essential for fly-by accelerated arbitration.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.1, 3.2, 3.6, 3.6.2.2 Figs.: 3-1, 3-2, 3-8, 3-9, 3-12 Pages: 19-20, 26-27, 29 IEEE 1394a-2000 (Jun. 30, 2000) Secs.: 3.9.2.3, 4.4.2.2, 4.4.3, 4.4.3.4.1, 4.4.3.4.2, 4.4.3.4.3 Figs.: 3-32, 4-25 Tables: 4-43, 4-47A, 4-47B Pages: 18, 70, 72, 83-85, 87-88
US 6,944,705	Claim 1 is essential for performing an autocrossover.	IEEE 1394b-2002 (Dec. 14, 2002) Sec.: 12.4.5, 16.2.5.1 Figs.: 19-3, 19-5, 19-7 Pages: 110, 181, 263, 269, 281, 287, 288

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
US 6,985,981	Claim 1 is essential for loop prevention.	IEEE 1394b-2002 (Dec. 14, 2002) Secs.: 3.10.5.2, 14.7, 14.7.1, 14.7.6, 14.7.7, 14.7.8, 14.7.9, 16.3.3.5, Fig.: 14-3 Table: 16-8 Pages: 27, 155, 156, 158, 159, 193, 194
US 5,848,240	Claim 1 is essential for controlling a communications channel between two nodes using the CHANNELS_AVAILABLE register and the oPCR or iPCR.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 2.2.49, 3.1, 3.2.1, 3.5.2, 6.2.2.3.2, 8.4.3.2 Figs.: 3-1, 3-2, 6-14 Pages: 15, 19-20, 24, 25, 152, 233 IEC 61883-1 (Feb. 1998) Secs.: 7.2, 7.3, 7.9, 8.2.1, 8.2.3, 8.3 Figs.: 15, 16, 18, 21 Pages: 21, 23, 29, 31, 33, 35, 61, 63, 67
US 5,699,521	Claim 8 is essential for a node link layer that updates the CYCLE_TIME register immediately with information in a received cycle start packet identified by the Tcode.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.2, 3.4, 3.4.1, 3.5.3, 6.2.2.2.3, 6.2.4.5, 6.3.3, 6.3.3.2 Figs.: 3-2, 3-4, 3-7, 6-10, 6-19 Tables: 6-9 Pages: 20, 22-23, 26, 148-149, 157-158, 166, 168-169
JP 3,151,103	Claim 1 is essential for a node link layer that updates the CYCLE_TIME register immediately with information in a received cycle start packet identified by the Tcode.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.4, 6.2.2.2.3, 6.2.4.5, 6.3.3.2 Tables: 6-9 Pages: 22-23, 148-149, 157-158, 168-169

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
JP 2,022,582	Claim 1 is essential for transmitting isochronous data of multiple different signals with multiple different nominal data rates on a 1394 bus.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.6.4, 6.2.3, 6.2.3.1, 6.3.1.5, 6.3.1.6, 6.3.3.3, 8.4.2.8, 8.4.3, 8.4.3.1 Figs.: 3-16, 6-17 Tables: 6-6 Pages: 32-33, 154-155, 164, 170, 231-232</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 1, 6.2, 7.2, 7.7 Figs.: 4 Tables: 6, 7 Pages: 9, 17, 21, 23, 27, 29, 43, 45, 49</p>
US 5,886,983	Claim 7 is essential for providing the oMPR and oPCR in a node on a tree topology 1394 bus.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.3, 4.2, 4.2.1, 4.2.1.1 Figs.: 3-3, 4-1 Pages: 21, 53-55</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 1, 4.1, 7.1, 7.2, 7.4 to 7.6, 7.9, A Figs.: 10, 12, A.1 Pages: 9, 11, 21, 23, 25, 27, 29, 55, 57, 79, 81, 83</p>
EP 776,560 (DE, FR, GB, IT)	Claim 9 is essential for providing the oPCR in a node on a tree topology 1394 bus.	<p>IEEE 1394-1995 (Aug. 30, 1996) Sec.: 1.1 Page: 1</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 1, 7.1, 7.2, 7.4, 7.7 Fig.: 12 Pages: 9, 21, 23, 25, 27, 29, 57</p>

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
JP 3,638,287	Claim 1 is essential for an isochronous resource manager node implementing the BANDWIDTH_AVAILABLE register and the CHANNELS_AVAILABLE register.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 2.2.38, 8.1.2, 8.3.2.3.8, 8.4.3, 8.4.3.1, 8.4.3.2 Fig.: 3-2 Pages: 15, 20, 199, 216, 217, 232, 233</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 7.1, 7.4, 7.7 Fig.: 12 Pages: 21, 23, 25, 27, 29, 57</p>
KR 392,282	Claim 8 is essential for providing the oPCR in a node on a tree topology 1394 bus.	<p>IEEE 1394-1995 (Aug. 30, 1996) Sec.: 1.1 Page: 1</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 1, 7.1, 7.2, 7.3, 7.4, 7.7, 7.9 Figs.: 8, 12 Pages: 9, 21, 23, 25, 27, 29, 31, 53, 58</p>
US 6,088,332	Claim 9 is essential for providing the oMPR and oPCR in a node on a tree topology 1394 bus.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.1, 3.2.1, 3.3, 3.4, 4.2, 4.2.1, 4.2.1.1 Figs.: 3-1, 3-3, 3-4, 4-1 Pages: 19 to 23, 53 to 55</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 1, 4.1, 7.1, 7.2, 7.3, 7.5, 7.7, 7.9, A Figs.: 8, 10, 12, A.1, Pages: 9, 11, 21, 23, 25, 27, 29, 53, 55, 57, 79, 81</p>

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
EP 1,195,950 (DE, FR, GB, IT)	Claim 6 is essential for providing the oMPR and oPCR in a node on a tree topology 1394 bus.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 1, 2.2.38, 3.1, 3.4, 3.7.3, 6.2.4.13, 8.1, 8.1.2 Figs.: Pages: 19, 22, 36-37, 159, 199</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 1, 6.1.1, 6.1.2, 7.1, 7.4, 7.7, 7.9 Figs.: 12 Pages: 9, 15, 21, 23, 25, 27, 29, 31, 57</p>
JP 3,638,946	Claim 1 is essential for an isochronous resource manager node implementing the BANDWIDTH_AVAILABLE register and the CHANNELS_AVAILABLE register.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 2.2.38, 8.1.2, 8.3.2.3.8, 8.4.3, 8.4.3.1, 8.4.3.2 Fig.: 3-2 Pages: 15, 20, 199, 216, 217, 232, 233</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 7.1, 7.4, 7.7 Figs.: 2, 12 Pages: 21, 23, 25, 27, 29, 49, 57</p>
EP 1,533,946 (DE, FR, GB, IT)	Claim 1 is essential for isochronous communication.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 1.1, 2.2.35, 8.1.2, 8.4.3.2, Annexes B, C Pages: 1, 15, 199, 233, 253, 254, 255</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 1, 5.2, 6.1.2, 7.1, 7.2, 7.4, 7.7, 7.9, 8.2.1 Figs.: 7, 8, 12, 16 Pages: 9, 11, 15, 21, 23, 25, 27, 29, 31, 33, 53, 57, 61</p>

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
EP 1,560,371 (DE, FR, GB, IT)	Claim 1 is essential for providing the oMPR and oPCR in a node on a tree topology 1394 bus.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 1, 1.4.3, 3.1, 3.7.3, 4, 8.1, 8.2, 8.4.3.1, Annex A Fig.: 3-2 Pages: 1, 3, 19, 20, 36, 37, 49, 199, 231, 232, 243</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 1, 7.1, 7.4, 7.7 Fig.: 12 Table: 6 Pages: 9, 21, 23, 25, 27, 29, 43, 57</p>
EP 1,696,608 (DE, FR, GB, IT)	Claim 7 is essential for bandwidth allocation.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 1, 1.4.3, 3.1, 3.7.3, 4, 8.1, 8.1.2, 8.3.2.3.7, 8.4.3.1, Annex A Fig.: 8-11 Pages: 1, 3, 19, 36, 37, 49, 199, 216, 232, 233, 243</p> <p>IEC 61883-1 (Feb. 2008) Secs.: 1, 7.1, 7.2, 7.4, 7.7 Fig.: 12 Tables: 7, 8 Pages: 7, 17, 18, 20 to 25</p>
EP 1,701,481 (DE, FR, GB, IT)	Claim 1 is essential for bandwidth allocation.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 1, 1.4.3, 3.1, 3.7.3, 8.1, 8.1.2, 8.3.2.3.7, 8.4.3.1, Annex A Fig.: 8-11 Pages: 1, 3, 19, 36, 37, 199, 216, 232, 233, 243</p> <p>IEC 61883-1 (Feb. 2008) Secs.: 1, 7.1, 7.4, 7.7 Fig.: 12 Table: 7 Pages: 7, 17, 20 to 25</p>

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
EP 1,703,676 (DE, FR, GB, IT)	Claim 6 is essential for isochronous communication.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 1, 1.4.3, 2.2.35, 3.1, 3.6.4, 3.7.3, 4.2, 4.2.1.1, 8.1, 8.1.2, 8.4.3.1 Pages: 1, 3, 15, 19, 32, 33, 36, 37, 53 to 55, 199, 232, 233</p> <p>IEC 61883-1 (Feb. 2008) Secs.: 1, 6.1, 7.1, 7.4, 7.7 Fig.: 2 Pages: 7, 12, 17, 20, 21, 23 to 25</p>
JP 3,667,748	Claim 6 is essential for isochronous communication.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 4, 8.1.2, 8.4.3.2, Annexes A, B, C Pages: 49, 199, 233, 243, 253, 255</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 5, 6.1.2, 7.1, 7.2, 7.4, 7.7 Figs.: 2, 7, 8, 9, 12, 16 Pages: 11, 15, 21, 23, 25, 27, 29, 49, 53, 55, 57, 61</p>
JP 3,667,751	Claim 7 is essential for bandwidth allocation.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 2.2.38, 4, 6.2.3.1, 8.1.2, 8.3.2.3.7, 8.4.2.3, 8.4.3.1, Annex A Fig.: 8-11 Pages: 15, 49, 154, 155, 199, 216, 229, 230, 232, 233, 243</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 7, 7.1, 7.2, 7.4, 7.7, 7.9 Figs.: 7, 8 Table: 6, 7 Pages: 21, 23, 25, 27, 29, 31, 43, 45, 53</p>

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
JP 3,930,028	Claim 1 is essential for isochronous communication.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 2.2.38, 4, 8.3.2.3.8, 8.4.3.1, 8.4.3.2, Annex A Figs.: 3-22, 3-23, 3-24 Pages: 115, 38, 39, 40, 49, 216, 217, 232, 233, 243</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 5.2, 6.1.2, 6.2.2, 7.1, 7.2, 7.4, 7.7 Figs.: 9, 12 Pages: 11, 15, 19, 21, 23, 25, 27, 29, 55, 57</p>
US 6,675,247	Claim 1 is essential for loop prevention.	<p>IEEE 1394b-2002 (Dec. 14, 2002) Sec.: 0.1, 3.10.5.2, 3.10.7.2, 14.1, 14.5, 14.7, 14.7.5, 14.7.8, 14.7.11, 16 Figs.: 14-1, 14-2, 16-1 Table: 14-2 Pages: 1, 27, 28, 33, 145, 147, 149, 150, 153, 155, 157, 158, 160, 173</p>
KR 225,043	Claim 1 is essential for transmitting the DATA_PREFIX signal.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 2.2.49, 3.6, 3.7.3.2 Figs.: 3-8, 3-9, 3-25, 3-26, 3-27, 3-28, 3-29 Table: 4-27 Pages: 15, 26, 27, 40 to 43, 85</p>

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
US 5,535,208	Claim 6 is essential for making and breaking point-to-point isochronous communications.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 4.2, 6.2.3.1, 8.4.3, 8.4.3.1, 8.4.3.2 Figs.: 4-1, 6-17 Pages: 53-54, 154-155, 232-233</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 7.3, 7.4, 7.9, 8.2, 8.2.1, 8.2.2 Figs.: 9, 15-18 Pages: 23, 25, 29, 31, 33, 55, 61, 63</p>
EP 682,430 (DE, FR, GB)	Claim 1 is essential for continuing an isochronous communication after a bus reset.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 4.2, 4.4.2.1, 4.4.2.2, 4.4.2.3, 6.2.3 Pages: 53-54, 98, 100, 103, 154</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 6, 6.2.1, 8.1, 8.2.1, 8.2.2, 8.2.3 Figs.: 16, 17, 18 Pages: 15, 17, 19, 31, 33, 61, 63</p>
EP 957,608 (DE, FR, GB)	Claim 1 is essential for bandwidth and channel allocation.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 4, 6.2.4.13, 6.3.1.6, 8.4.2.6, 8.4.3, 8.4.3.1, 8.4.3.2 Pages: 49, 159, 164, 231, 232-233</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 6 Pages: 15</p>

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
EP 957,610 (DE, FR, GB)	Claim 1 is essential for establishing a point-to-point connection.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 4, 6.2.4.13, 6.3.1.6, 7.3.1.3, 7.3.4.3, 8.4.3, 8.4.3.1, 8.4.3.2 Pages: 49, 159, 178, 187-188, 232-233</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 6, 7.1, 7.2, 7.3, 7.7, 7.9, 8.1, 8.2.1, 8.2.2 Figs.: 16, 17 Pages: 15, 21, 23, 27, 29, 31, 33, 61, 63</p>
JP 3,127,704	Claim 1 is essential for transmitting the node_ID and isochronous data in isochronous packets with a CIP header on a 1394 bus.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 2.2.63, 3.2, 3.2.1, 3.4.1, 6.2.3.1 Figs.: 3-2, 6-17 Pages: 16, 20, 23, 154-155</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 6.1.1, 6.2.1 Figs.: 2, 5(a)-(b) Pages: 15, 17, 49, 51</p>
JP 3,152,055	Claim 1 is essential for transmitting isochronous packets via a 1394 bus on default channel of a broadcast out connection.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.2, 3.2.1, 3.4.1, 6.2.3.1, 8.4.3, 8.4.3.1, 8.4.3.2 Figs.: 3-2 Pages: 20, 23, 154-155, 232-233</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 7.3, 7.5, 7.7, 8.3.1 Figs.: 12, 19 Pages: 23, 25, 27, 29, 35, 57, 65</p>

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
US 5,646,941	Claim 14 is essential for transmitting isochronous packets formatted with the CIP header.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.2.1, 3.6.4, 8.3.2.3.7 Figs.: 3-2, 3-16 Pages: 20, 32-33, 216</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 6.1.1, 6.1.2, 6.1.3, 6.2.1 Figs.: 2, 3, 5 Tables: 3 Pages: 15, 17, 19, 41, 49, 51</p>
EP 687,113 (DE, FR, GB)	Claim 1 is essential for isochronous packets with a packet header structure and CIP header structure.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 6.2.3.1 Figs.: 6-17 Pages: 154, 155</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 6.1.1, 6.1.2, 6.1.3 Figs.: 2, 3 Pages: 15, 17, 49</p>
US 6,128,316	Claim 1 is essential for allocating bandwidth, including necessary overhead corresponding to topology changes.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.1, 3.2.1, 8.4.3.1 Figs.: 3-1, 3-9 Pages: 19-20, 27, 232</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 7.7 Fig.: 12 Table: 7 Pages: 27, 29, 45, 57</p>

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
EP 862,295 (DE, ES, FR, GB, IT)	Claim 1 is essential for bandwidth allocation, including necessary overhead that can be modified.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 8.4.3, 8.4.3.1 Pages: 232-233</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 7.1, 7.2, 7.7, 7.9, 7.11 Figs.: 12 Pages: 21, 23, 27, 29, 31, 57</p>
JP 3,146,928	Claim 1 is essential for allocating, for isochronous communication on a 1394 bus, an amount of bandwidth determined from the maximum payload size, data rate and overhead_ID specified in the oPCR.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.2, 3.2.1, 3.4.1, 8.4.3, 8.4.3.2 Figs.: 3-2 Pages: 20, 23, 232-233</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 7.7 Tables: 7 Pages: 27, 29, 45</p>
US 6,567,421	Claim 1 is essential for allocating bandwidth, including necessary overhead.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.1, 3.2.1, 8.4.3.1 Figs.: 3-1, 3-9 Pages: 19-20, 27, 232</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 5.3.1, 7.1, 7.7, 7.9, 7.11 Fig.: 12 Table: 7 Pages: 13, 21, 27, 29, 31, 45, 57</p>

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
US 6,577,646	Claim 1 is essential for allocating bandwidth for maximum size of isochronous packets and necessary overhead.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.1, 3.2.1, 8.4.3.1 Figs.: 3-1, 3-9 Pages: 19-20, 27, 232</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 6.1.1, 7.3, 7.7 Figs.: 2, 12 Table: 7 Pages: 15, 23, 27, 29, 45, 49, 57</p>
US 6,587,477	Claim 1 is essential for allocating bandwidth, including necessary overhead.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.1, 3.2.1, 8.4.3.1 Figs.: 3-1, 3-9 Pages: 19-20, 27, 232</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 6.1.1, 7.7 Figs.: 2, 12 Table: 7 Pages: 15, 27, 29, 45, 49, 57</p>
EP 1,193,927 (DE, ES, FR, GB, IT)	Claim 1 is essential for allocating bandwidth, including necessary overhead.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 1.1, 8.4.3.1 Pages: 1, 232</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 7.1, 7.2, 7.7, 7.9 Fig.: 12 Table: 7 Pages: 21, 23, 27, 29, 31, 45, 57</p>

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
JP 3,129,143	Claim 1 is essential for transmitting isochronous packets with a CIP header on a 1394 bus.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.2, 3.2.1, 3.4.1, 6.2.3.1 Figs.: 3-2, 6-17 Pages: 20, 23, 154-155</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 6.1.3 Figs.: 2, 3, 5(a)-(b) Pages: 15, 17, 49, 51</p>
JP 3,152,233	Claim 1 is essential for transmitting isochronous packets via a 1394 bus by setting the channel number and incrementing the point-to-point counter of the oPCR of the output device.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.2, 3.2.1, 3.4.1, 6.2.3.1, 8.4.3, 8.4.3.1, 8.4.3.2 Figs.: 3-2 Pages: 20, 23, 154-155, 232-233</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 7.3, 7.4, 7.7, 8.2.1 Figs.: 9, 12, 16 Pages: 23, 25, 27, 33, 55, 57, 61</p>
JP 3,156,707	Claim 1 is essential for bandwidth allocation including overhead.	<p>IEEE 1394-1995 (Aug. 30, 1996) Sec.: 8.4.3.1 Page: 232</p> <p>IEC 61883-1 (Feb. 1998) Sec.: 7.7 Pages: 27, 29</p>

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
JP 3,156,711	Claim 1 is essential for bandwidth allocation, where the contents of the oPCR can be read by other devices.	<p>IEEE 1394-1995 (Aug. 30, 1996) Sec.: 8.4.3.1 Page: 232</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 7.7, 7.9 Fig.: 12 Pages: 27, 29, 31, 57</p>
JP 3,156,715	Claim 1 is essential for bandwidth allocation, where the contents of the oPCR can be read by other devices.	<p>IEEE 1394-1995 (Aug. 30, 1996) Sec.: 8.4.3.1 Page: 232</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 7.7, 7.9 Fig.: 12 Pages: 27, 29, 31, 57</p>
JP 3,341,758	Claim 1 is essential for continuing an isochronous communication after a bus reset.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.7.3.1.1, 3.7.3.1.2, 3.7.3.1.3, 6.2.3, 6.2.3.1 Fig.: 6-17 Pages: 38-39, 154, 155</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 6.1.1, 6.2.1, 7.3, 7.4, 8.2.1, 8.5 Figs.: 5, 15, 25, 26 Pages: 15, 17, 23, 25, 33, 37, 51, 69, 71</p>

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
KR 335,534	Claim 1 is essential for bandwidth allocation, including overhead.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.2, 8.4.3.1 Figs.: 3-2 Pages: 20, 232, 233</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 7.2, 7.7, 7.9 Figs.: 7, 12 Table: 7 Pages: 21, 27, 29, 31, 45, 53, 57</p>
KR 340,355	Claim 1 is essential for bandwidth allocation, including overhead.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 8.3.2.3.7, 8.4.3.1 Figs.: 8-11 Pages: 216, 232</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 6.1.1, 7.2, 7.7, 7.9 Figs.: 2, 7, 12 Pages: 15, 21, 27, 29, 31, 49, 53, 57</p>
KR 340,356	Claim 1 is essential for bandwidth allocation, including overhead.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 8.3.2.3.7, 8.4.3.1 Figs.: 8-11 Pages: 216, 232</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 6.1.1, 7.2, 7.7, 7.9 Figs.: 2, 7, 12 Pages: 15, 21, 27, 29, 31, 49, 53, 57</p>

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
KR 340,357	Claim 1 is essential for bandwidth allocation, including overhead.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 8.3.2.3.7, 8.4.3.1 Figs.: 8-11 Pages: 216, 232 IEC 61883-1 (Feb. 1998) Secs.: 6.1.1, 7.2, 7.7, 7.9 Figs.: 2, 7, 12 Pages: 15, 21, 27, 29, 31, 49, 53, 57
KR 208,371	Claim 1 is essential for transmitting a data prefix and subsequent data packet.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.6, 4.3.3, 6.2.2.3, 6.2.3.1, 6.2.4.8, 6.2.4.11, 6.2.4.15 Figs.: 3-8, 3-9, 6-12, 6-17 Table: 4-27 Pages: 26, 27, 85, 150, 151, 154, 155, 158, 159, 160
US 5,539,390	Claim 1 is essential for forwarding self_ID packets containing the PHY_ID each node assigns to itself.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.1, 3.7.3.1.3, 4.3.4.1, 4.4.2.3, 4.4.2.3.1, 4.4.2.3.2 Figs.: 3-1, 3-24, 4-18, 4-24 Tables: 4-29, 4-38, 4-41, 4-46, 4-47 Pages: 19, 39-40, 86-87, 94, 96, 103-105, 111
EP 722,224 (DE, FR, GB, NL)	Claim 1 is essential for self-identification in a serial bus system with a controller and at least two controlled apparatuses in a tree structure.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.7.3, 3.7.3.1.2, 3.7.3.1.3, 4.3.4.1, 4.4.2.2, 4.4.2.3, 4.4.2.3.1 Figs.: 3-23, 3-24, 4-18, 4-23, 4-24 Pages: 36, 38-40, 86, 100, 103-104

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
US 5,621,725	Claim 7 is essential for transmitting isochronous data formatted according to the CIP format including the DBC field.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.4.1, 3.6.4, 6.3.1.6 Figs.: 3-16 Pages: 23, 32-33, 164</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 6.2, 6.2.1 Figs.: 4 Pages: 17, 19, 49</p>
KR 365,537	Claim 1 is essential for transmitting isochronous data formatted according to the CIP format including the DBC field.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.4.1, 6.3.1.5, 6.3.1.6 Pages: 23, 164</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 6.1.1, 6.2, 6.2.1, 7.2 Figs.: 4, 5, 7 Pages: 15, 17, 19, 21, 23, 49, 51, 53</p>
US 5,689,244	Claim 7 is essential for modifying PCR values using lock commands.	<p>IEEE 1394-1995 (Aug. 30, 1996) Sec.: 3.5.2 Tables: 7-5 Pages: 25, 188</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 7.1, 7.2, 7.3, 7.9 Figs.: 7 Pages: 21, 23, 29, 53</p>

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
EP 689,296 (AT, DE, ES, FR, GB, NL)	Claim 7 is essential for modifying PCR values using lock commands.	IEEE 1394-1995 (Aug. 30, 1996) Sec.: 3.5.2, 7.3.1.3 Tables: 7-5 Pages: 25, 178, 188 IEC 61883-1 (Feb. 1998) Secs.: 1, 7.1, 7.2, 7.9 Pages: 9, 21, 23, 29
KR 497,689	Claim 1 is essential for restoring connections after a bus reset.	IEC 61883-1 (Feb. 1998) Secs.: 7.2, 7.3, 7.10, 8.3, 8.3.1, 8.5 Fig.: 7 Pages: 21, 23, 31, 33, 35, 37, 53
KR 504,312	Claim 1 is essential for restoring connections after a bus reset.	IEC 61883-1 (Feb. 1998) Secs.: 7.2, 7.7, 7.8, 7.10, 8.5 Figs.: 7, 12, 13 Pages: 27, 29, 31, 37, 53, 57, 59
US 5,790,876	Claim 2 is essential for placing a node in the suspended or active state for conserving power.	IEEE 1394a-2000 (Jun. 30, 2000) Secs.: 3.9.5, 3.9.5.2, 3.9.5.4, 4.2.2, 4.4.4 Figs.: 4-12, 4-26 Pages: 20-23, 43-44, 91-93
US 5,944,827	Claim 2 is essential for placing a node in the suspended or active state for conserving power.	IEEE 1394a-2000 (Jun. 30, 2000) Secs.: 3.9.5, 3.9.5.2, 3.9.5.4, 4.2.2, 4.4.4 Figs.: 4-12, 4-26 Pages: 20-23, 43-44, 91-93
EP 727,729 (DE, FR, GB, NL)	Claim 1 is essential for placing a node in the suspended or active state for conserving power.	IEEE 1394a-2000 (Jun. 30, 2000) Secs.: 3.9.5, 3.9.5.2, 3.9.5.4, 4.2.2, 4.4.4 Figs.: 4-12, 4-26 Pages: 20-23, 43-44, 91-94

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
US 5,949,761	Claims 12 and 19 are essential for an isochronous resource manager node implementing the BANDWIDTH_AVAILABLE register, and the CHANNELS_AVAILABLE register.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.1, 3.2.1, 3.6.4, 8.1.2, 8.1.4, 8.3.2.3.7, 8.3.2.3.8, 8.4.2.3, 8.4.2.5, 8.4.3, 8.4.3.1, 8.4.3.2 Figs.: 3-2, 3-16, 8-11, 8-12 Pages: 19, 20, 33, 199, 216, 217, 229-230, 232-233
JP 3,194,318	Claim 1 is essential for an isochronous resource manager node implementing the BANDWIDTH_AVAILABLE register, and the CHANNELS_AVAILABLE register.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 8.3.1.5, 8.3.2.3.7, 8.3.2.3.8, 8.4.3, 8.4.3.1, 8.4.3.2 Pages: 204, 216-217, 232-233
US 5,978,360	Claims 12 and 19 are essential for an isochronous resource manager node implementing the BANDWIDTH_AVAILABLE register, and the CHANNELS_AVAILABLE register.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.1, 3.2.1, 3.6.4, 8.1.2, 8.1.4, 8.3.2.3.7, 8.3.2.3.8, 8.4.2.3, 8.4.2.5, 8.4.3, 8.4.3.1, 8.4.3.2 Figs.: 3-2, 3-16, 8-11, 8-12 Pages: 19, 20, 33, 199, 216, 217, 229-230, 232-233
EP 674,410 (DE, FR, GB, IT, NL)	Claim 1 is essential for an isochronous resource manager node implementing the BANDWIDTH_AVAILABLE register, and the CHANNELS_AVAILABLE register.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 1.1, 3.4.1, 3.5.2, 3.6.4, 8.1.2, 8.3.1.5, 8.3.2.3.7, 8.3.2.3.8, 8.4.3, 8.4.3.1, 8.4.3.2 Figs.: 3-16, 8-11, 8-12 Pages: 1, 23-25, 33, 199, 204, 216-217, 232-233
US 5,995,489	Claims 12 and 14 are essential for an isochronous resource manager node implementing the BANDWIDTH_AVAILABLE register, and the CHANNELS_AVAILABLE register.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.1, 3.2, 3.2.1, 3.6.4, 3.7.3.1.2, 3.7.3.1.3, 4.4.2.2, 4.4.2.2.1, 4.4.2.3, 4.4.2.3.1, 8.1.2, 8.1.4, 8.3.1.6, 8.3.2.3.7, 8.3.2.3.8, 8.4.2.3, 8.4.2.5, 8.4.3, 8.4.3.1, 8.4.3.2 Figs.: 3-2, 3-16, 3-23, 3-24, 4-23, 4-24, 8-11, 8-12 Tables: 4-45, 4-46 Pages: 19, 20, 33, 38-40, 100-106, 199, 204, 216, 217, 229-230, 232-233
US 6,024,606	Claim 8 is essential for implementing a 4-pin connector plug.	IEEE 1394a-2000 (June 30, 2000) Secs.: 4.2.1A.1.1 Figs.: 4-11A, 4-11B Pages: 25-26

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
JP 3,064,874	Claims 4 and 5 are essential for implementing a 4-pin connector plug.	IEEE 1394a-2000 (June 30, 2000) Secs.: 4.2.1A.1.1 Figs.: 4-11A, 4-11B Pages: 25-26
KR 435,915	Claim 1 is essential for implementing a 4-pin connector plug.	IEEE 1394a-2000 (June 30, 2000) Secs.: 4.2.1A.1.1, 4.2.1A.1.6 Figs.: 4-11A, 4-11B Pages: 25-26, 29
KR 435,916	Claim 1 is essential for implementing a 4-pin connector plug.	IEEE 1394a-2000 (June 30, 2000) Secs.: 4.2.1A.1, 4.2.1A.1.1, 4.2.1A.1.5 Figs.: 4-11A, 4-11B Pages: 25-26, 29
US 6,185,622	Claim 1 is essential for a node with PHY and Link layers and a Configuration ROM storing the link_spd in the Bus_info_block.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.4, 4, 4.2.2, 4.3.1, 4.3.2, 4.3.3, 6, 6.2, 6.3.1.1 to 6.3.1.7 Figs.: 3-4, 4-12, 4-17, 6-1 Tables: 4-25 to 4-28 Pages: 22-23, 49, 76, 84-85, 137, 143, 163-165 IEEE 1394a-2000 (June 30, 2000) Secs.: 8.3.2.5.4 Figs.: 8-20 Tables: 5B-1 Pages: 126, 167, 168
US 6,243,362	Claims 1, 3 and 5 are essential for implementing the BANDWIDTH_AVAILABLE and CHANNELS_AVAILABLE registers.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.1, 3.2.1, 8.3.2.3.7, 8.3.2.3.8, 8.4.3, 8.4.3.1, 8.4.3.2 Figs.: 3-2, 8-11, 8-12 Pages: 19, 20, 216, 217, 232, 233
US 6,266,344	Claim 1 is essential for beta mode speed signaling.	IEEE P1394b (Oct. 15, 2001) Secs.: 3.2.26, 4, 4.4.2, 4.4.3, 4.4.4, 10.3.1.4 Table: 10-13 Pages: 34, 43, 45-46, 165

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
EP 834,815 (DE, FR, GB, IT, NL)	Claim 1 is essential for beta mode speed signaling.	IEEE P1394b (Oct. 15, 2001) Secs.: 3.2.26, 4, 4.4.2, 4.4.3, 4.4.4, 10.3.1.4 Table: 10-13 Pages: 34, 43, 45-46, 165
KR 516,565	Claim 1 is essential for beta mode speed signaling.	IEEE 1394-1995 (Aug. 30, 1996) Sec.: 3.2 Fig.: 3-2 Page: 20 IEEE 1394b (Dec. 14, 2002) Secs.: 3.10.4, 3.10.4.1, 3.10.4.2, 3.10.4.3, 13.3.1.4 Table: 13-3 Pages: 24, 25, 135-136
US 6,275,473	Claims 1, 3 and 5 are essential for implementing the BANDWIDTH_AVAILABLE and CHANNELS_AVAILABLE registers.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.1, 3.2.1, 4.2.2, 8.3.2.3.7, 8.3.2.3.8, 8.4.3, 8.4.3.1, 8.4.3.2 Figs.: 3-2, 4-12, 8-11, 8-12 Pages: 19, 20, 76, 216, 217, 232, 233
US 6,299,486	Claims 1 and 4 are essential for a 4-pin connector plug.	IEEE 1394a-2000 (June 30, 2000) Secs.: 4.2.1A.1.1 Figs.: 4-11A, 4-11B, 4-11D, 4-11E Pages: 25-26, 28
US 6,330,249	Claims 1, 3 and 5 are essential for implementing the BANDWIDTH_AVAILABLE and CHANNELS_AVAILABLE registers.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.1, 3.2.1, 8.3.2.3.7, 8.3.2.3.8, 8.4.3, 8.4.3.1, 8.4.3.2 Figs.: 3-2, 8-11, 8-12 Pages: 19, 20, 216, 217, 232, 233

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
US 6,381,697	Claim 5 is essential for transmission of an FCP frame.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.1, 3.6.3, 3.6.4, 4.4.1.2, 6.2, 6.3.1.3 Figs.: 3-16, 6-1 Table: 4-41 Pages: 19, 32-33, 96, 143, 163</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 1, 4.1, 9.1, 9.2, 9.3, 9.3.1 Figs.: 32, 33 Table: 8 Pages: 9, 11, 37, 39, 45, 77</p>
EP 1,024,417 (DE, FR, GB, NL)	Claim 16 is essential for transmission of an FCP frame.	<p>IEEE 1394-1995 (Aug. 30, 1996) Sec.: 4 Page: 49</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 1, 9.1, 9.3, 9.3.1 Table: 8 Pages: 9, 37, 39</p>
KR 458,144	Claim 5 is essential for transmission of an FCP frame.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.4.1 Pages: 23</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 6.2, 9.1, 9.2, 9.3, 9.3.1 Figs.: 29, 30, 31, 32, 33 Table: 8 Pages: 17, 37, 39, 45, 73, 75, 77</p>

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
US 6,816,485	Claim 1 is essential for controlling a connection using a PCR.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 2.2.36, 3.4, 3.4.1, 6.2.3, 6.2.3.1, 8.3.1.3, 8.3.1.4, 8.3.1.5 Figs.: 6-17 Table: 6-6 Pages: 15, 22-23, 154-155, 203-204</p> <p>IEEE 1394a-2000 (June 30, 2000) Sec.: 6.2.3A Page 136</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 6.2.2, 7.1, 7.2, 7.3, 7.4, 7.7, 7.8, 9.2 Figs.: 2, 7, 8, 9, 12, 13, 14, 16 Pages: 19, 21, 23, 25, 27, 29, 39, 49, 53, 55, 57, 59, 61</p>
EP 658,010 (DE, FR, GB, NL)	Claim 1 is essential for controlling a connection using a PCR.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 2.2.27, 2.2.35, 3.4.1, 4, 6.2.2, 6.2.3, 6.2.3.1 Figs.: 6-3 Table: 6-3, 6-6 Pages: 14, 15, 23, 49, 145-46, 154-55</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 1, 7.1, 7.2, 7.8, 7.9 Figs.: Pages: 9, 21, 29, 31</p>
JP 3,194,380	Claim 1 is essential for isochronous resource management using BANDWIDTH_AVAILABLE and CHANNELS_AVAILABLE registers.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 8.3.1.5, 8.3.2.3.7, 8.3.2.3.8, 8.4.3, 8.4.3.1, 8.4.3.2 Pages: 204, 216-217, 232-233</p>

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
JP 3,194,381	Claim 1 is essential for isochronous resource management using BANDWIDTH_AVAILABLE and CHANNELS_AVAILABLE registers.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 8.3.1.5, 8.3.2.3.7, 8.3.2.3.8, 8.4.3, 8.4.3.1, 8.4.3.2 Pages: 204, 216-217, 232-233
JP 3,194,382	Claim 1 is essential for isochronous resource management using BANDWIDTH_AVAILABLE and CHANNELS_AVAILABLE registers.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 8.3.1.5, 8.3.2.3.7, 8.3.2.3.8, 8.4.3, 8.4.3.1, 8.4.3.2 Pages: 204, 216-217, 232-233
JP 3,219,085	Claim 1 is essential for isochronous resource allocation after a bus reset.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 8.3.1.5, 8.3.2.3.7, 8.3.2.3.8, 8.4.2.3, 8.4.2.4, 8.4.2.8, 8.4.3, 8.4.3.1, 8.4.3.2 Pages: 204, 216-217, 229-230, 231-233
JP 3,221,442	Claim 1 is essential for isochronous resource management using BANDWIDTH_AVAILABLE and CHANNELS_AVAILABLE registers.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 8.3.1.5, 8.3.2.3.7, 8.3.2.3.8, 8.4.3, 8.4.3.1, 8.4.3.2 Pages: 204, 216-217, 232-233
JP 3,304,633	Claim 7 is essential for modifying PCR values using lock commands.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.5.2, 7.3.1.3, 7.3.4.3 Fig.: 3-6 Tables: 7-5 Pages: 24, 25, 178, 187, 188 IEC 61883-1 (Feb. 1998) Secs.: 7.2, 7.9 Figs.: 11, 16 Pages: 21, 23, 29, 31, 57, 61

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
JP 3,307,085	Claim 1 is essential for modifying a PCR to establish a connection.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.2, 3.4.1 Fig.: 3-2 Pages: 20, 23</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 7.2, 7.3, 7.7, 7.8, 7.9 Pages: 21, 23, 27, 29, 31</p>
JP 3,438,259	Claim 1 is essential for transmitting a block-bearing or empty isochronous packet each cycle.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.:3.2, 3.4.1 Fig.: 3-2 Pages: 20, 23</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 6.2, 6.2.1, 6.2.2 Fig.: 4 Pages: 17, 19, 49</p>
JP 3,478,293	Claim 1 is essential for bandwidth allocation, including bandwidth for overhead and maximum payload size.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.:3.1, 3.2 Figs.: 3-1, 3-2 Pages: 19, 20</p> <p>IEC 61883-1 (Feb. 1998) Sec.: 7.7 Pages: 27, 29</p>

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
JP 3,500,785	Claim 1 is essential for bandwidth allocation, including bandwidth for overhead and maximum payload size, and channel allocation.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 8.3.2.3.7, 8.3.2.3.8, 8.4.3.1, 8.4.3.2 Pages: 216, 217, 232, 233</p> <p>IEC 61883-1 (Feb. 1998) Sec.: 7.7 Pages: 27, 29</p>
JP 3,690,409	Claim 1 is essential for point to point connections.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.4.1, 3.5.1, 3.6.4, 6.2.2, 6.2.4.2.1, 6.2.4.7, 8.4.3.2 Figs.: 3-16, 6-17 Pages: 23, 24, 32, 33, 145, 156, 158, 233</p> <p>IEC 61883-1 (Feb. 1998) Sec.: 7.2, 7.3, 7.7, 7.9, 8.2, 8.2.1, 8.2.2, 8.2.3 Figs.: 8, 9, 12, 15, 16, 17 Pages: 21, 23, 27, 29, 31, 33, 53, 55, 57, 61, 63</p>
JP 3,702,888	Claim 1 is essential for re-establishing a point-to-point connection after a bus reset.	<p>IEEE 1394-1995 (Aug. 30, 1996) Sec.: 3.4.1 Page: 23</p> <p>IEC 61883-1 (Feb. 1998) Sec.: 6.2, 7.2, 7.3, 7.7, 7.8, 7.10, 9.2 Figs.: 7, 12, 13 Pages: 17, 21, 23, 27, 29, 31, 39, 53, 57, 59</p>
KR 295,220	Claim 1 is essential for isochronous resource management using BANDWIDTH_AVAILABLE and CHANNELS_AVAILABLE registers.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.4.1, 3.8, 8.3.1.5, 8.3.2.3.7, 8.3.2.3.8, 8.4.2.3, 8.4.3, 8.4.3.1, 8.4.3.2 Figs.: 3-4, 8-11, 8-12 Pages: 22, 23, 47, 204, 216-217, 229-230, 232-233</p>

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
KR 359,672	Claim 1 is essential for modifying a PCR to establish a connection.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.2, 3.4.1 Fig.: 3-2 Pages: 20, 23</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 6.2, 7.2, 7.3, 7.7, 7.8, 7.9, 9.2 Figs.: 7, 12, 13, 14, 29 Pages: 17, 21, 23, 27, 29, 31, 39, 53, 57, 59, 73</p>
KR 365,609	Claim 1 is essential for controlling a connection using a PCR.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.2, 3.4.1 Fig.: 3-2 Pages: 20, 23</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 6.2, 7.2, 7.3, 7.7, 7.8, 9.2 Figs.: 7, 8, 12, 13, 29 Pages: 17, 21, 23, 27, 29, 39, 53, 57, 59, 73</p>
KR 395,385	Claim 1 is essential for bandwidth allocation, including bandwidth for overhead and maximum payload size.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.2, 3.4.1 Fig.: 3-2 Pages: 20, 23</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 6.2, 7.2, 7.3, 7.7, 9.2 Figs.: 7, 8, 12, 29 Pages: 17, 21, 23, 27, 29, 39, 53, 57, 73</p>

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
KR 403,109	Claim 1 is essential for modifying PCR values using lock commands.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.5, 3.5.1, 3.5.2 Fig.: 3-6 Pages: 23, 24, 25</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 7.2, 7.3, 7.9 Figs.: 7, 10, 11, 12, 13 Pages: 21, 23, 29, 31, 53, 55, 57, 59</p>
KR 403,401	Claim 1 is essential for modifying PCR values using lock commands.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.5, 3.5.1, 3.5.2, 8.4.3.2 Fig.: 3-6 Pages: 23, 24, 25, 233</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 7.2, 7.3, 7.9 Figs.: 7, 10, 11, 12, 13 Pages: 21, 23, 29, 31, 53, 55, 57, 59</p>
US 5,341,371	Claim 16 is essential for data-strobe encoded communication.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.7.1, 3.7.3, 4.2.2, 4.3.1, 4.4, 5.3.1, 5.4 Figs.: 3-17, 4-12, 4-17, 4-21, 5-1, 5-5, 5-7 Pages: 34, 36, 76, 83, 92, 117, 125, 128</p>
EP 458,648 (DE, FR, GB, IT)	Data bit and clock coding via transmission of Data and Strobe signal. Strobe signal changes whenever two consecutive data bits are the same.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 1.1, 2.2.52, 3.7.1, 4.2.2, 4.3.1, 4.4, 4.4.1.1, 4.4.1.2, 5.3.1 Figs.: 3-17, 4-12, 4-21 Pages: 1, 16, 34, 76, 83, 92, 94-97, 125</p>
JP 3,359,346	Claim 12 is essential for data-strobe encoded communication.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.7.1, 4.3.1, 5.3.1 Figs.: 3-17, 4-17, 5-5 Pages: 34, 83, 125</p>

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
US 5,535,214	Claim 7 is essential for prioritizing asynchronous and isochronous packet transmission at the root node.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.1, 3.7.3.2, 4.1.2.1, 4.4.2.4, 4.4.2.4.1, 4.4.2.4.2, 5.4.1, 5.4.1.1 to 5.4.1.6 Figs.: 3-1, 3-25 to 3-29, 4-25, 5-8 to 5-10 Pages: 19, 40-43, 51-52, 106-112, 129-132
EP 575,053 (DE, FR, GB, NL)	Claim 1 is essential for determining an order for, and a rearrangement of, transmission requests and processing transmission according to the rearrangement.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 1.1, 1.4.3, 3.6.3, 3.6.4, 3.7.3.2, 4.2.2, 4.3.7, 4.4, 4.4.2.4, 4.4.2.4.1, 5.4.1, 5.4.1.2, 5.4.1.3, 5.4.1.5 Figs.: 1-1, 3-16, 4-12, 4-21 Pages: 1, 3, 32-33, 40-43, 76, 90, 92-93, 106-110, 129-132
KR 112,949	Claim 1 is essential for prioritizing asynchronous and isochronous packet transmission at the root node.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.1, 3.7.3.2, 4.4.2.4, 4.4.2.4.1, 4.4.2.4.2, 5.4.1, 5.4.1.1 to 5.4.1.6 Figs.: 3-1, 3-25 to 3-29, 4-25, 5-8 to 5-10 Pages: 19, 40-43, 51-52, 106-112, 129-132
JP 3,080,811	Claim 19 is essential for storing the topology map during the self_ID process and transmitting asynchronous packets afterward.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.1, 3.2.1, 3.4.1, 3.7.3.1.3, 4.2, 4.3.4.1, 4.4.2.3.1, 6.2.2, 6.2.4.2.1, 8.3.1.6, 8.4.6, 8.4.6.1 Figs.: 3-1, 3-2, 4-1, 4-18, 6-3 Tables: 4-29, 6-3, 6-7 Pages: 19-20, 23, 39-40, 53-54, 86-87, 104, 145, 156, 204, 236
JP 3,249,334	Claim 1 is essential for bandwidth allocation.	IEEE 1394-1995 (Aug. 30, 1996) Secs.: 8.3.2.3.7, 8.4.3.1 Pages: 216, 232, 233 IEC 61883-1 (Feb. 1998) Secs.: 7.7, 7.11 Pages: 27, 29, 31

<u>Patent No.</u>	<u>Description</u>	<u>Standard Portion</u>
JP 3,251,934	Claim 1 is essential for urgent arbitration.	<p>IEEE 1394-1995 (Aug. 30, 1996) Secs.: 3.7.4.2 Pages: 45, 46</p> <p>IEC 61883-1 (Feb. 1998) Secs.: 7.7, 7.11 Pages: 27, 29, 31</p>